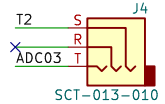
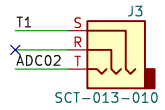
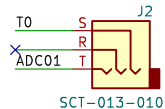


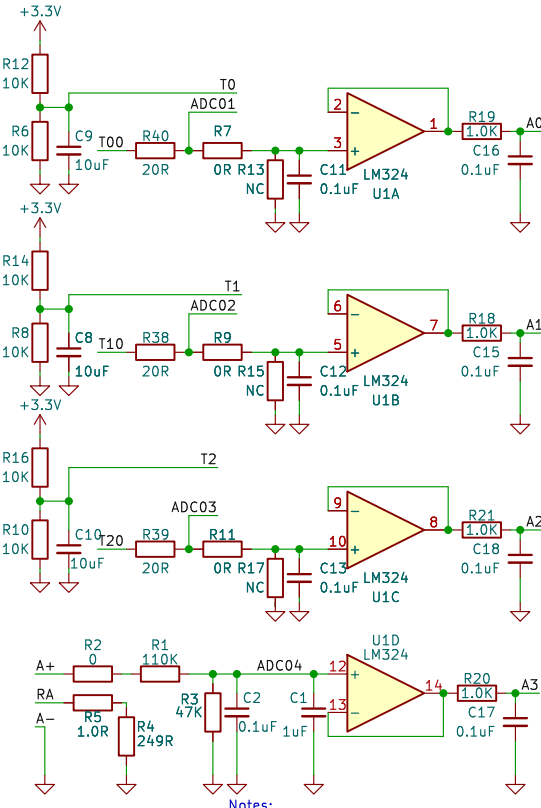
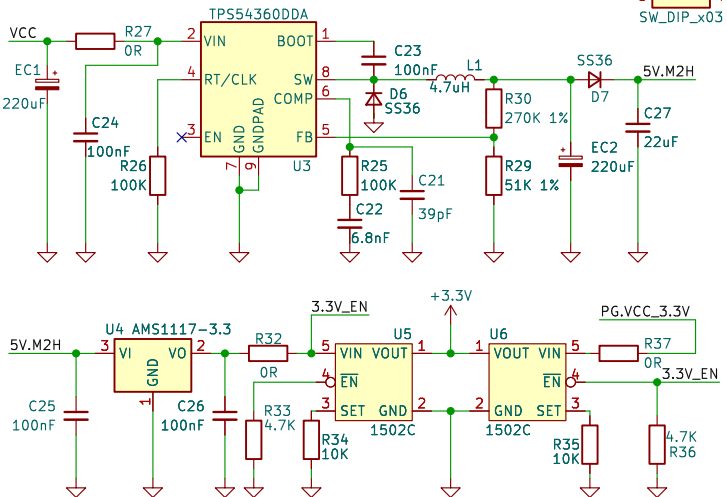
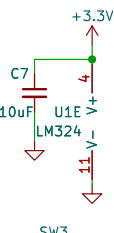
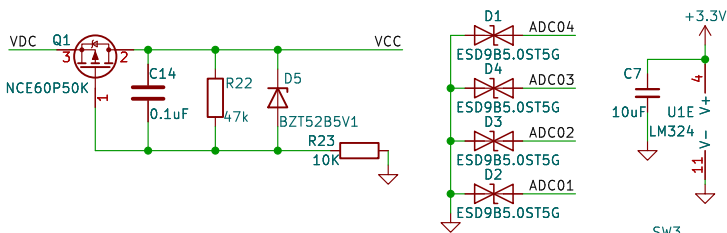
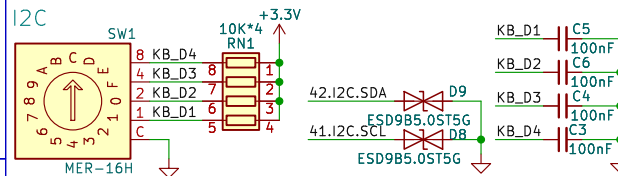
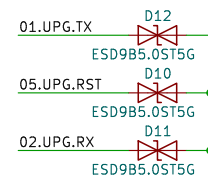
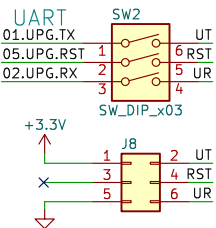
Notes:
 1. J1 / OUTPUT, Phoenix contact 3.5x2
 MAXCurrent: 5A
 2. J1 / INPUT, DCJACK 2.0
 5-24V, MAXCurrent: 5A
 3. J2-4 / INPUT, 3.5mm headphone jack
 0-1V or 50mA max
 4. J5 / INPUT, Phoenix contact 3.5x3
 0-10V Or 20MA max

CONNECTOR POGO PIN



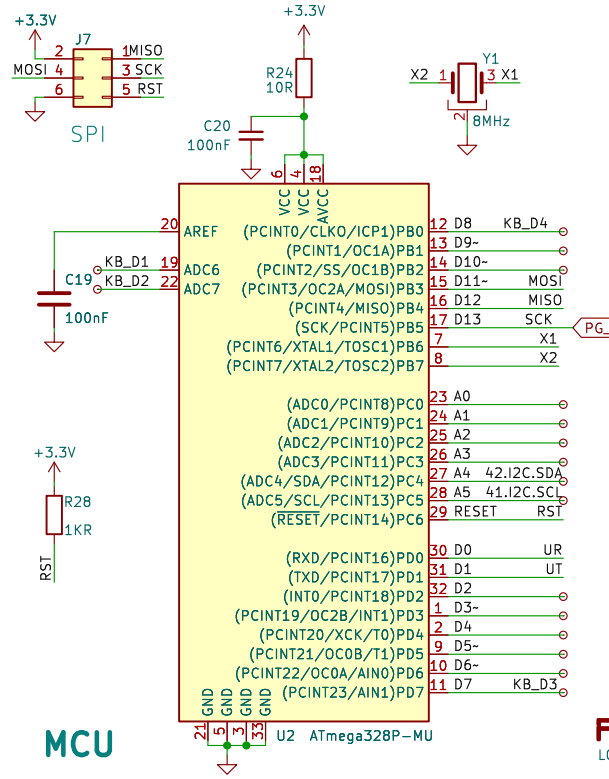
GND	12	J9	1	PG.VCC_3.3V
20.USB.D+	13		2	41.I2C.SCL
19.USB.D-	14		3	42.I2C.SDA
12.INT	15		4	09.SPI.SCK
44.USB.RX	16		5	40.SPI.MOSI
43.USB.TX	17		6	06.SPI.MISO
13.FREE	18		7	05.UPG.RST
14.FREE	19		8	02.UPG.RX
45.FREE	20		9	01.UPG.TX
04.FREE	21		10	5V.M2H
5V.H2M	22		11	GND

pogo in 2x11



Notes:
 1. When testing the current input gear, insert this jumper cap into positions 2-3!
 2. Please disconnect the jumper cap at positions 2-3 to disconnect the sampling resistor for the default test voltage range.

U_i=10V
 U_o=U_i*(47K/(110K+47K))
 =10*0.29936=2.99V



POWER

MCU



Project	WF1-RIMU	Name	WF1-Current_Monitor_4	Num	0x0004	Designer	H.B	I2C	Y	UART	Y
	https://rimu.me		community.rimu.me		rimu@dengtec.com	Date	2024-12-10	SPI	N	UPGRADE	Y
						Rev	0.6		A4		1/1